Automatic Loop Transformation Selection with the Aid of Kohonen’s Self-Organizing Maps for Parallelizing Compilers

Mohamed Ahamed Mead¹, Hesham Eldeeb², and Salwa Nassar³

¹ Faculty of Science, Cairo University
Cairo, Egypt
mohmead@yahoo.com

² Computers and systems Department, Electronics Research Institute
Giza, Egypt
heldeeb@mcit.gov.eg

³ Head of parallel and Distributed Systems Team, Computers and systems Department, Electronics Research Institute
Giza, Egypt
salwa@eri.sci.eg

Abstract

Determine the appropriate loop transformations is an essential process in the automatic parallelization field. The sequence of loop transformation to be applied also must be considered. Selection of loop transformations faces many challenges, it is needed an experienced. In this paper, An Intelligent Loop Transformation Selector (ILTS); as a part of parallelizing tool project, was developed to overcome on these challenges and imitate an experienced. A Kohonen’s Self-Organizing Map (SOM) neural network is used to select the appropriate loop transformation or sequence of them. Neural Networks offer intelligent transformations selection to reduce or eliminate the dependencies and maximize the parallelization in the sequential code. The experimental results show that ILTS chooses loop transformations successfully in most cases. This tool can be integrated with any parallelizing compiler to enhance loop transformation selection process.

Keywords: Automatic code parallelization, Dependence Analysis, Loop Transformation, Neural Networks, Kohonen Network.

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References


Developing an Intelligent Layer for Automatic Parallel Detection Implemented on Different High Performance Computing Platform

Mohamed Ahamed Mead¹, Hesham Eldeeb², and Salwa Nassar³

1 Faculty of Science, Cairo University
   Cairo, Egypt
   mohmead@yahoo.com

2 Computers and systems Department, Electronics Research Institute
   Giza, Egypt
   heldeeb@mcit.gov.eg

3 Head of parallel and Distributed Systems Team, Computers and systems Department, Electronics Research Institute
   Giza, Egypt
   salwa@eri.sci.eg

Abstract
In this paper, an automatic parallelization tool for C code, named Intelligent Automatic Parallel Detection Layer (IAPDL), is presented. It generates parallelized MPI code, and OpenMp code from the sequential code; at the loop level, to be executed on a cluster platform and multicore platform respectively. In addition to, a tool that uses a new approach to choosing loop transformations, called Intelligent Loop Transformation Selector (ILTS), is developed. It is designed as an integrated part in IAPDL. The selection process of appropriate loop transformation was accomplished intelligently; a Kohonen’s Self-Organizing Map (SOM) neural network is used to select the appropriate loop transformation or sequence of them.

Keywords: Automatic parallelization, Cluster of Workstations, Multicore Processor, Dependence Analysis, Loop Transformation, Neural Networks.

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Degrees
-B.Sc., Statistics and Computer Science, Mathematics Department, Faculty of Science, Ain Shames University, 1996.
-M.Sc., Computer Science, Mathematics Department, Faculty of Science, Cairo University, 2002.
-Ph.D., Computer Science, Mathematics Department, Faculty of Science, Cairo University, 2012.

Places of employ
-Lecturer in the faculty Of Computer Sciences & Information System @ Suez Canal University
  (From Jan 2013 to until now)
-Lecturer in the Higher Institute Of Computer Sciences & Information System @ Oct 6 City
  (From Jan 2012 Dec 2012)
-Lecturer in the technical college of Tabuk in Saudi Arabia
  (From Feb 2002 To July 2010)
-Programmer in Professional Company
  (From October 2000 To 2002)
-Programmer in Arab Net Company
  (From Dec 1999 To October 2000)
-Lecturer in Computer & System Lap, El Hegas Institute for specific studies
  (From October 1998 To June 1999)